ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention

TIMER LOCKOUT CIRCUIT FOR SYNCHRONOUS APPLICATIONS

Submision Type: Utility Patent Filing

Application Number:

10/604168

EFS ID: 42616

Server Response:

Confirmation Code	Message
	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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First Named Applicant: Mark Jacunski

Attorney Docket Number: BUR920020143US1

Timestamp: 2003-06-29 13:16:53 EDT

From: us

File Listing:

Doc.Name	File Name	Size
		(Bytes)
us-request	BUR920020143US1-usrequ.xml	3582
us-request	us-request.dtd	19064
us-request	us-request.xsl	33300
us-assignment	BUR920020143US1-usassn.xml	2182
us-assignment	us-assignment.xsl	8983
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us-assignment	BUR920020143assign1.tif	42916
us-assignment	BUR920020143assign2.tif	4420
us-fee-sheet	BUR920020143US1-usfees.xml	2151
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us-fee-sheet	us-fee-sheet.dtd	10901
us-declaration	BUR920020143Dec1.tif	47484
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us-declaration	BUR920020143Dec3.tif	29288
us-declaration	BUR920020143Dec4.tif	27880
application-body	BUR920020143SPECCOPY2-trans.xm	30751
application-body	us-application-body.xsl	83497
application-body	application-body.dtd	49498
application-body	wipo.ent	4956
application-body	mathmi2.dtd	54588
application-body	mathml2-qname-1.mod	13225
application-body	isoamsa.ent	5191
application-body	isoamsb.ent	3988
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application-body	isoamsr.ent	7073

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